

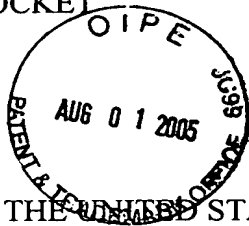
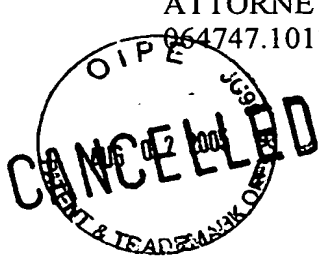
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ATTORNEY DOCKET
064747.1017

PATENT APPLICATION
10/825,021

1



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Shannon V. Davidson et al.
Serial No.: 10/825,021
Filing Date: April 15, 2004
Title: System and Method for Topology-Aware Job Scheduling and Backfilling in an HPC Environment

Mail Stop: Amendments
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450
Dear Sir:

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I hereby certify that this communication is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" under 37 C.F.R. § 1.10 on the date indicated below and is addressed to The Commissioner For Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

Willie Jiles
Willie Jiles

Date: August 1, 2005

INFORMATION DISCLOSURE STATEMENT

Applicants respectfully request, under 37 C.F.R. §§ 1.56, 1.97 and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified patent application. Copies of all references are enclosed for the convenience of the Examiner. No representation is made that a search has been made, that these references are material to the patentability, or that these references qualify as prior art.

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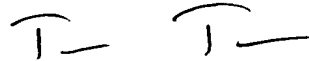
PATENT APPLICATION
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REMARKS

Each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than thirty days prior to the filing of this Information Disclosure Statement, and this Information Disclosure Statement is submitted before a first Office Action on the merits. Under 37 C.F.R. § 1.97(b)(3), no fee is due. Nonetheless, the Commissioner is hereby authorized to charge any fee and credit any overpayment to Deposit Account No. 02-0384 of Baker Botts L.L.P.

Respectfully submitted,
BAKER BOTTS L.L.P.
Attorneys for Applicants

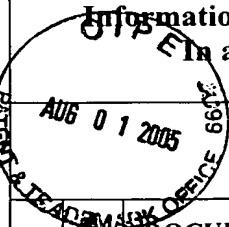
Handwritten signature of Travis W. Thomas, consisting of a stylized 'T' followed by a horizontal line.

Travis W. Thomas
Reg. No. 48,667

Date: August 1, 2005

Correspondence Address:

Customer Number 45507

PTO-1449		Application No. 10/825,021		Applicant(s) Shannon V. Davidson et al.		
Information Disclosure Citation in an Application 		Docket Number 064747.1017		Group Art Unit	Filing Date April 15, 2004	
		U.S. PATENT DOCUMENTS				
	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A						
B						
C						
D						
FOREIGN PATENT DOCUMENTS						
	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
NON-PATENT DOCUMENTS						
	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					DATE
E	Communication from the European Patent Office, European Search Report for Application No. EP 05 25 2235, 3 pages, 07/22/2005, 3 pages					07/22/2005
F	Rzymianowicz et al., "Clustering SMP Nodes with the ATOLL Network: A Look into the Future of System Area Networks," Proceedings of High Performance Computing, 8th International Conference, May 8, 2000, 10 pages					05/08/2000
G	Bhanot et al., "The BlueGene/L Supercomputer," 20th International Symposium on Lattice Field Theory," vol. 119, June 2002, 8 pages					06/2002
H	Advanced Micro Devices et al., "Hypertransport Technology I/O Link - A High-Bandwidth I/O Architecture," July 20, 2001, pp. 1-25					07/20/2001
I	Pinkston et al., "InfiniBand: The "De Facto" Future Standard for System and Local Area Networks or Just a Scalable Replacement for PCI Buses?," Cluster Computing-Kluwer Academic Publishers, vol. 6, no. 2, 2003					2003
J	Culler et al., "Parallel Computer Architecture - A Hardware/Software Approach, Interconnection Network Design," Morgan Kaufmann, 1999, 30 pages					1999
EXAMINER				DATE CONSIDERED		
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.						

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